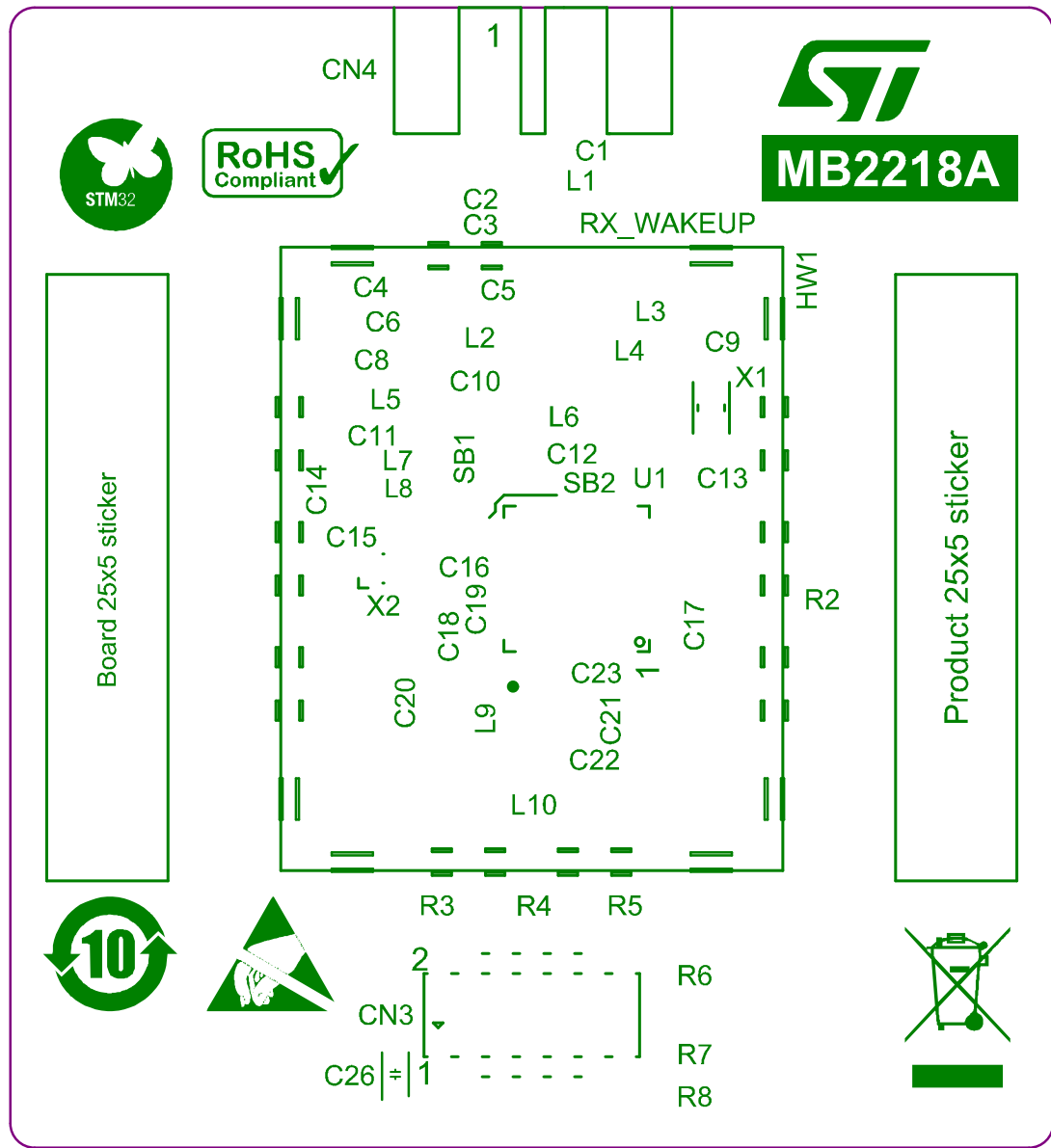
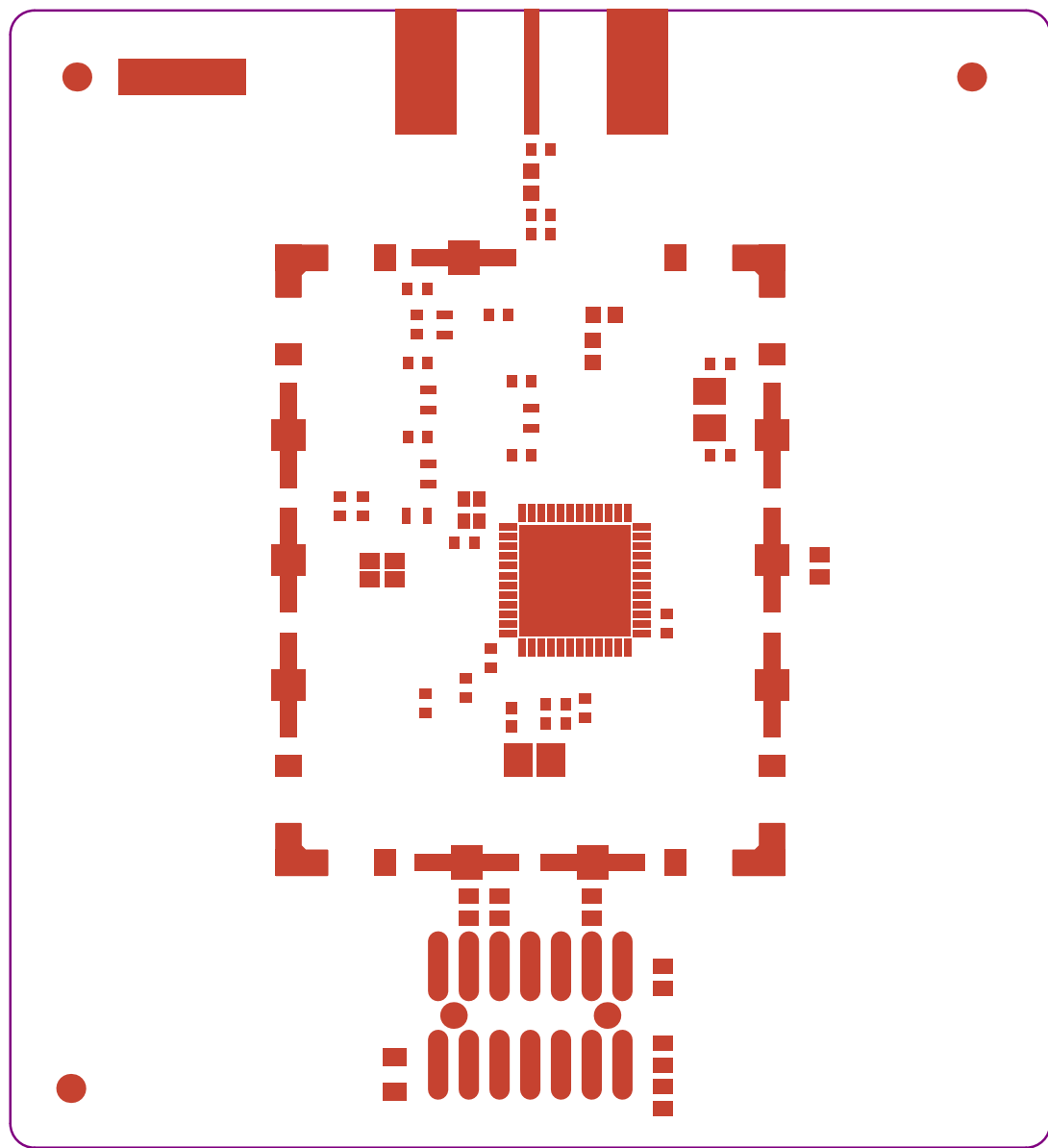
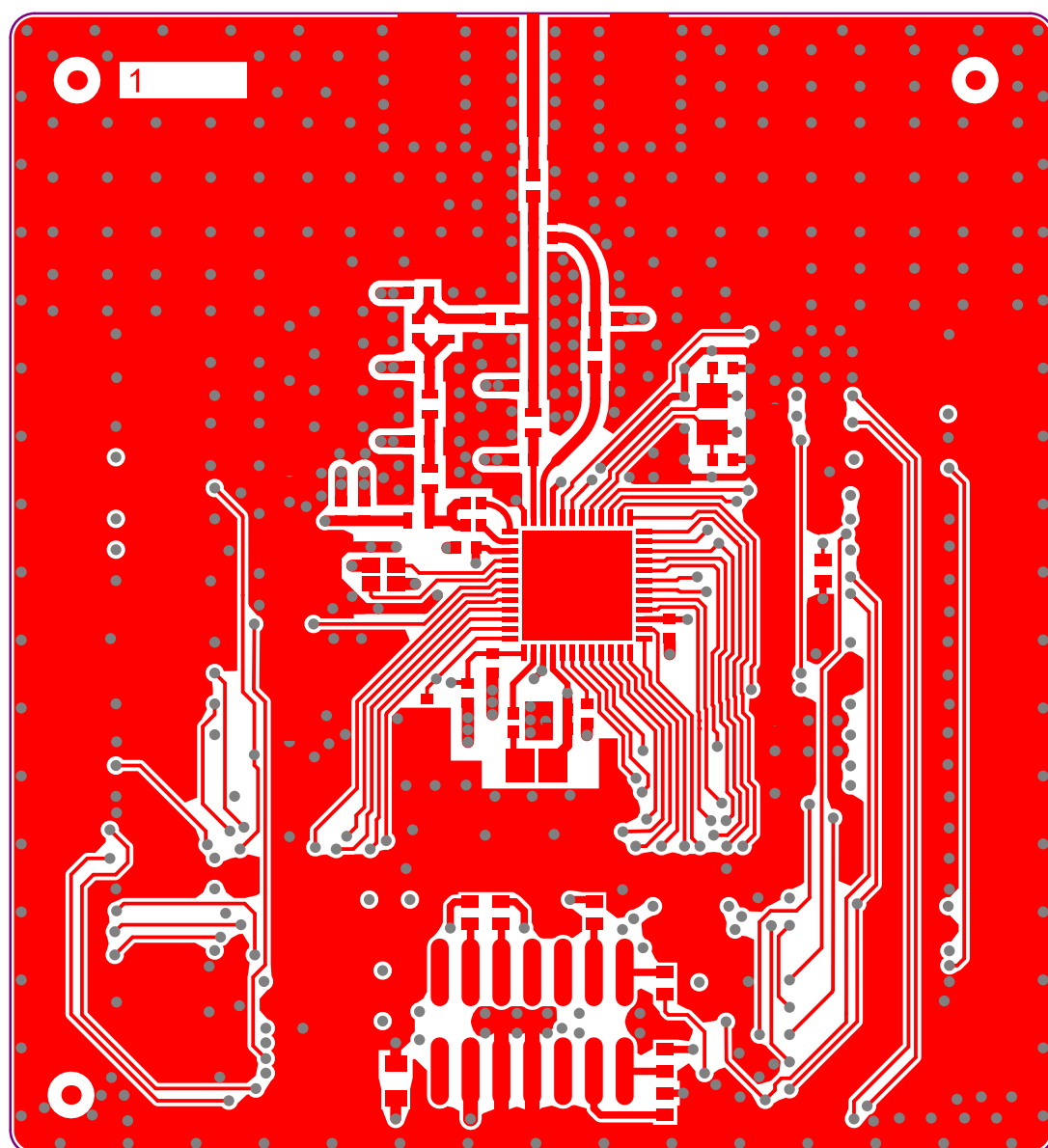




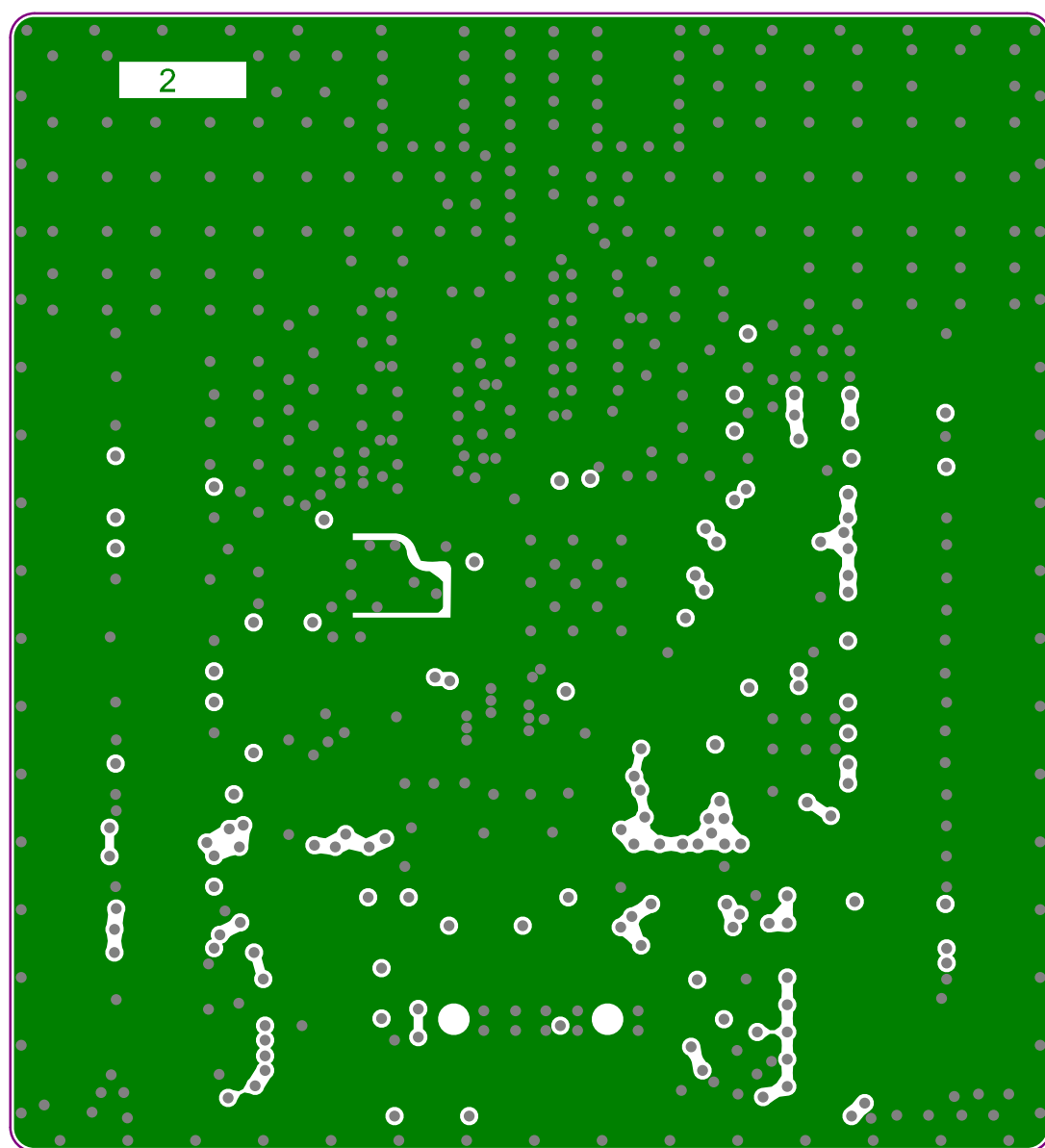
Project: STM32WL3 - QFN48 - 4-Layers - SMD		
Layer: Top Overlay	Gerber: .GTO	
Variant: [No Variations]	Ref: MB2218	
Date: 12/02/2024	Rev: A	



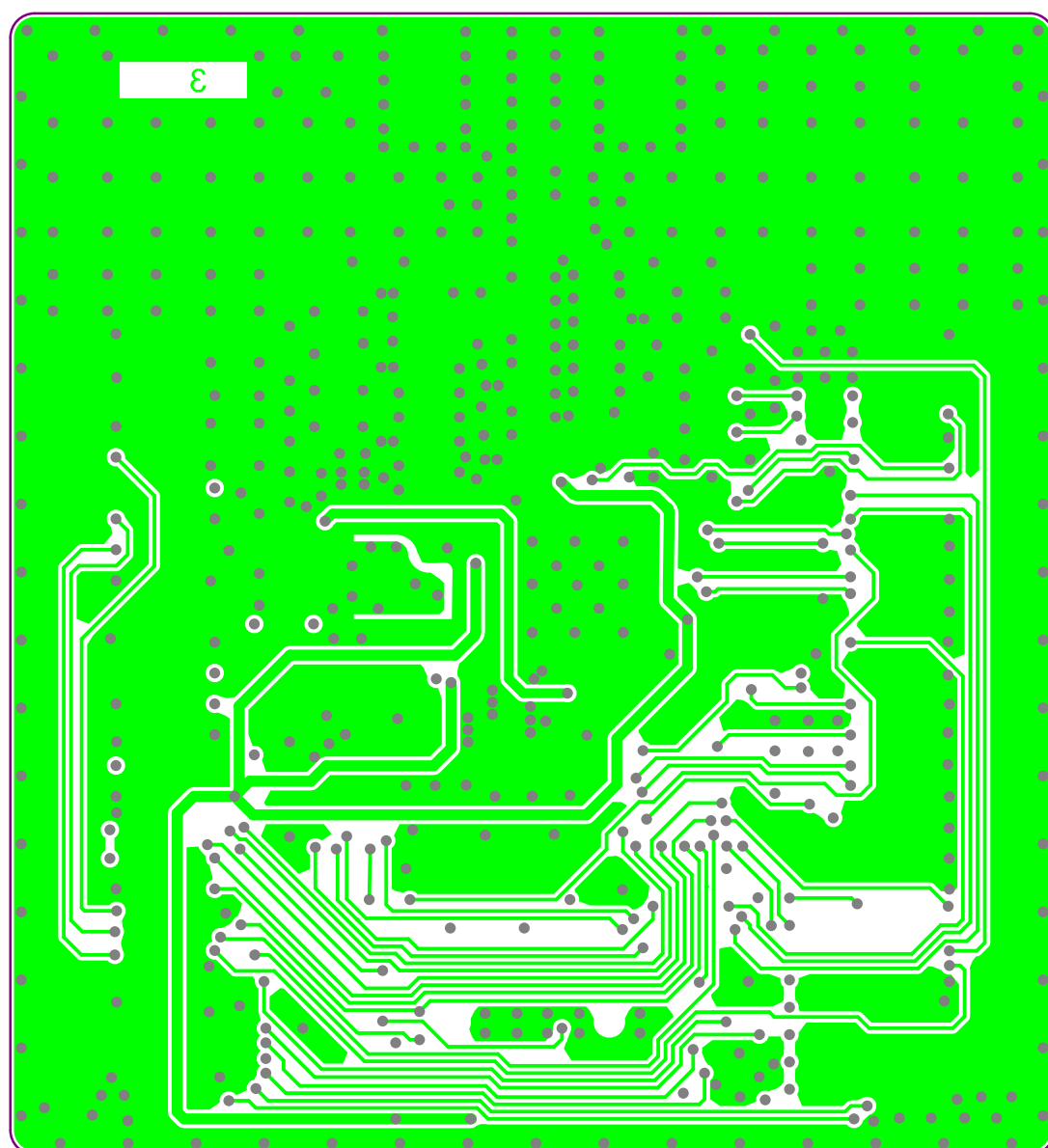
Project: STM32WL3 - QFN48 - 4-Layers - SMD		
Layer: Top Solder	Gerber: .GTS	
Variant: [No Variations]	Ref: MB2218	
Date: 12/02/2024	Rev: A	



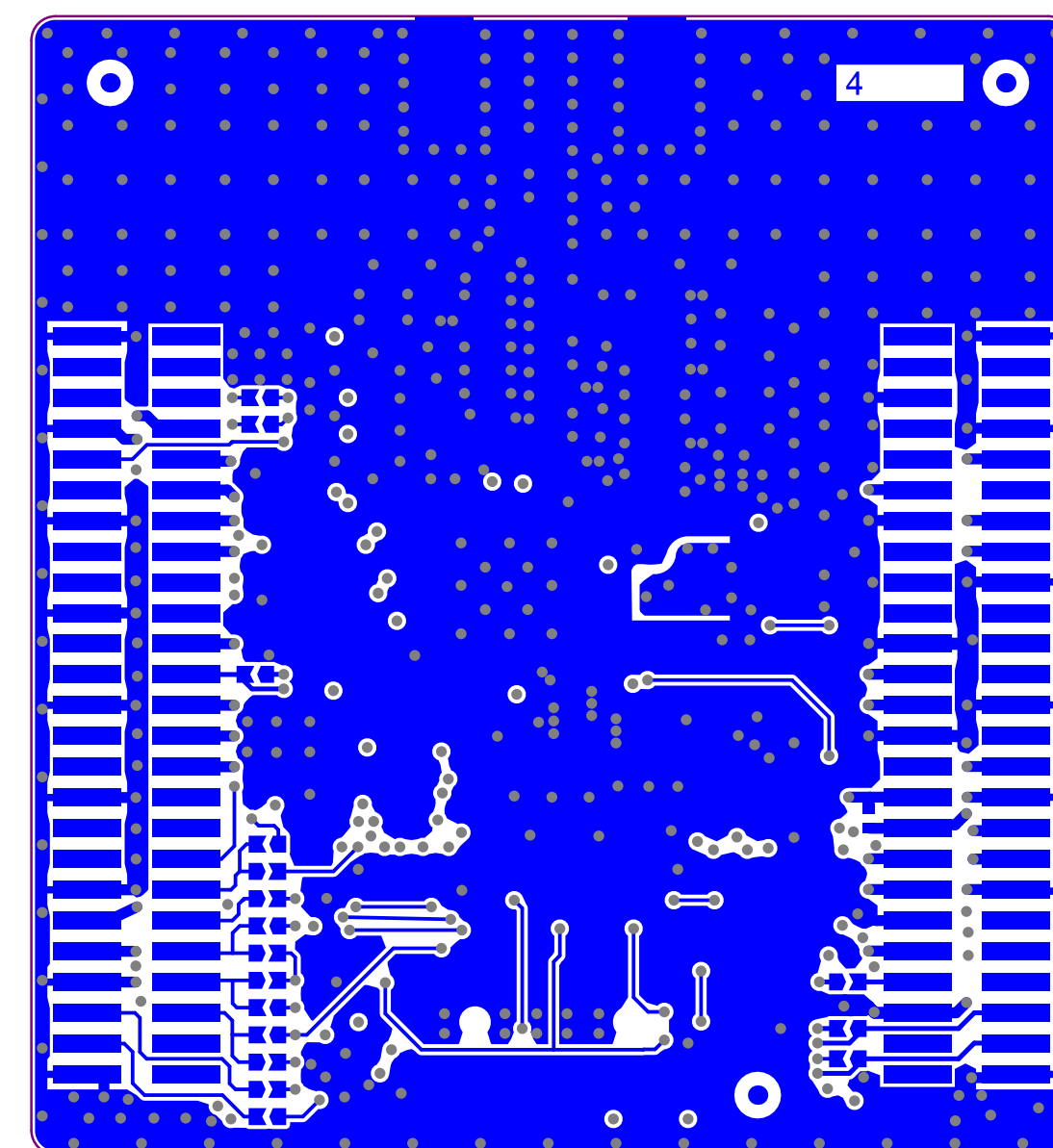
Project: STM32WL3 - QFN48 - 4-Layers - SMD		
Layer: Top Layer	Gerber: .GTL	
Variant: [No Variations]	Ref: MB2218	
Date: 12/02/2024	Rev: A	



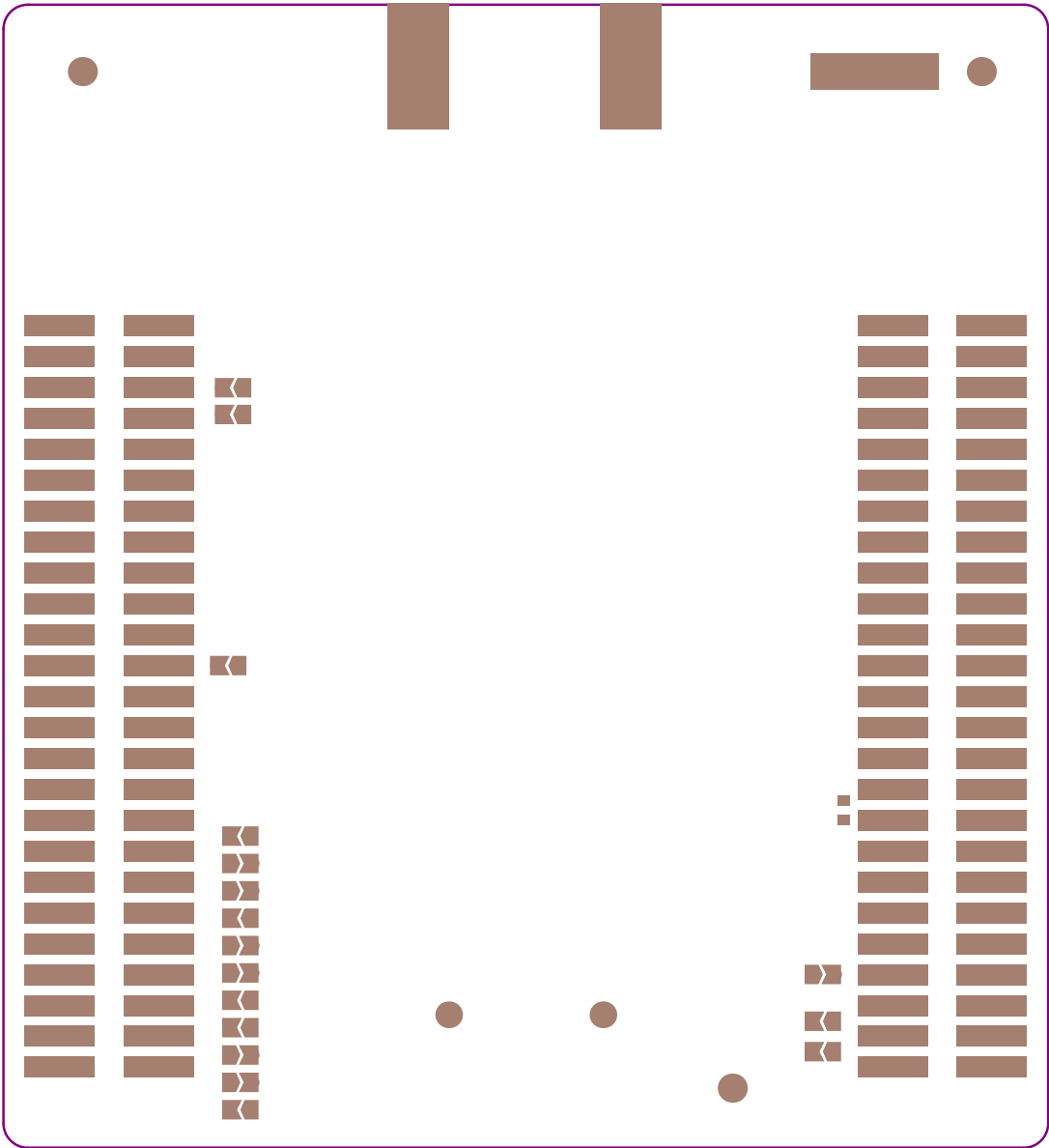
Project: STM32WL3 - QFN48 - 4-Layers - SMD		
Layer: Signal Layer 1	Gerber: .G1	
Variant: [No Variations]	Ref: MB2218	
Date: 12/02/2024	Rev: A	



Project: STM32WL3 - QFN48 - 4-Layers - SMD		
Layer: Signal Layer 2	Gerber: .G2	
Variant: [No Variations]	Ref: MB2218	
Date: 12/02/2024	Rev: A	

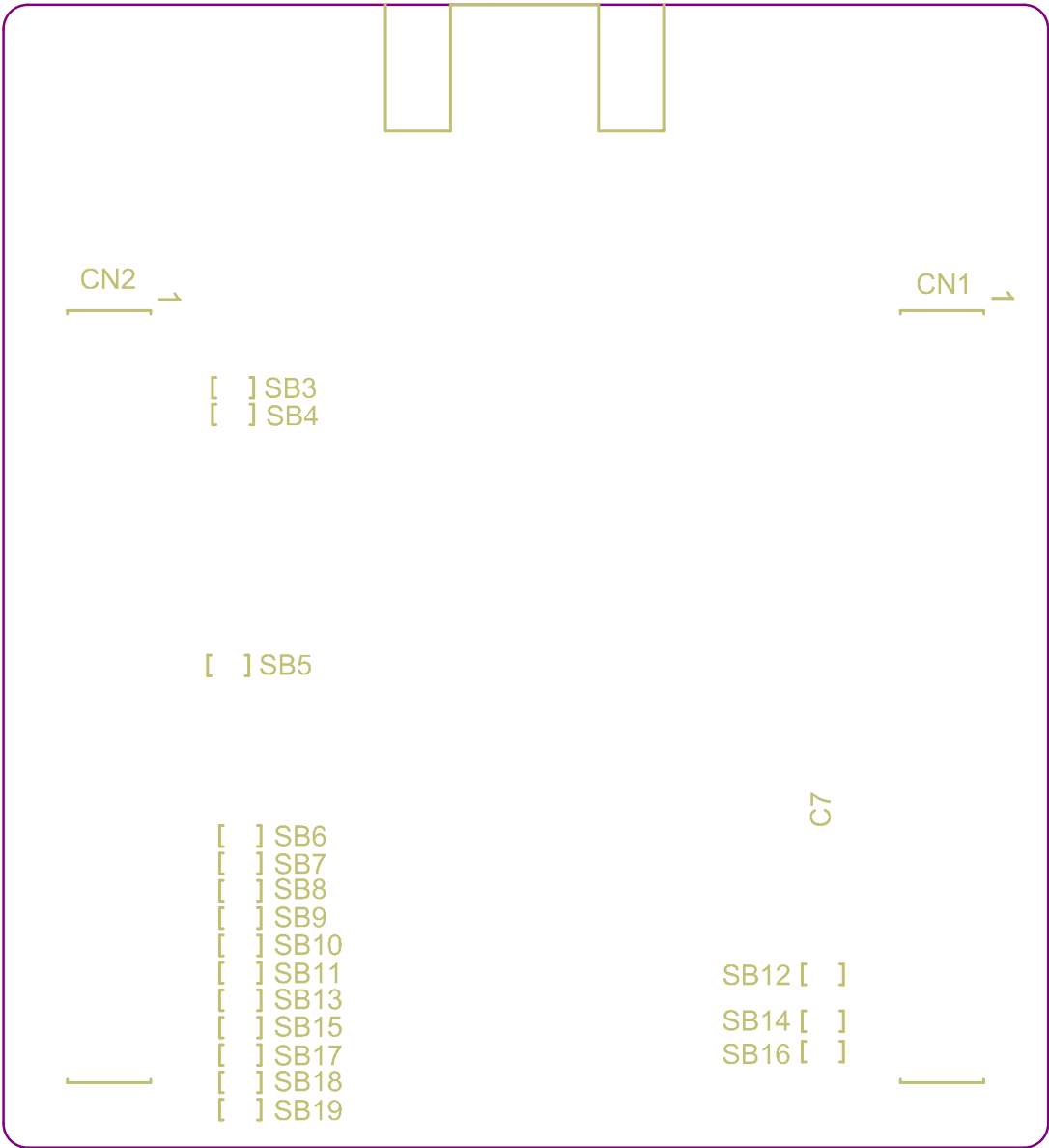


Project: STM32WL3 - QFN48 - 4-Layers - SMD		
Layer: Bottom Layer	Gerber:.GBL	
Variant: [No Variations]	Ref: MB2218	
Date: 12/02/2024	Rev: A	



Project: STM32WL3 - QFN48 - 4-Layers - SMD	
Layer: Bottom Solder	Gerber:.GBS
Variant: [No Variations]	Ref: MB2218
Date: 12/02/2024	Rev: A





Project: STM32WL3 - QFN48 - 4-Layers - SMD

Layer: Bottom Overlay

Gerber:.GBO

Variant: [No Variations]

Ref: MB2218

Date: 12/02/2024

Rev: A



PCB SPECIFICATIONS :

A. MATERIAL :

B. MATERIAL FAMILY :

C. SOLDERMASK COLOR :

D. SILKSCREEN COLOR :

E. SURFACE FINISH :

F. IMPEDANCE CONTROL :

G. THROUGH VIA :

H. STACK-UP :

FR-4

Ventec VT/47

☐ GREEN

☒ BLUE

☐ RED

☐ BLACK

☒ WHITE

☐ YELLOW

☐ BLACK

☒ ENIG

☐ IMMERSION SILVER

☐ IMMERSION TIN

☐ HASL

☐ HASL (PB-FREE)

☐ GOLDEN FINGER

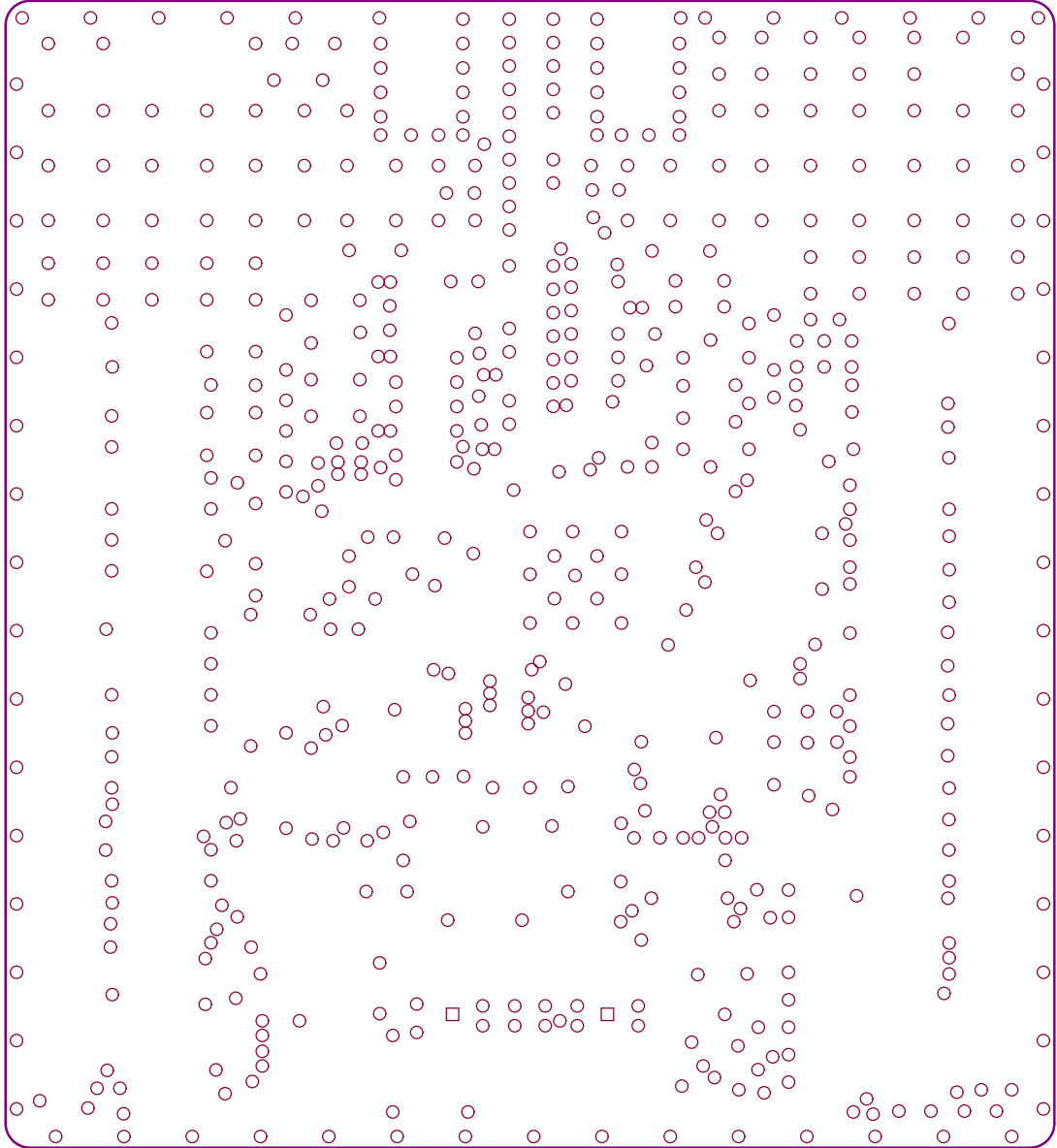
☐ NO

☒ YES

PLUG THE VIAS WHICH ARE COVERED WITH SOLDERMASK ONE OR TWO SIDE.

PLUG MATERIAL : ☒ SOLDERMASK ☐ NON-CONDUCTIVE EPOXY.

SEE LAYER STACK-UP SEQUENCE FOR OVERALL THICKNESS.



Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.010mm	3.5
1	Top Layer	Copper	0.041mm	
	Dielectric 1	FR4 VT-47	0.254mm	4
2	Signal Layer 1	Copper	0.018mm	
	Dielectric 2	FR4 VT-47	0.914mm	4.3
3	Signal Layer 2	Copper	0.018mm	
	Dielectric 3	FR4 VT-47	0.254mm	4
4	Bottom Layer	Copper	0.041mm	
	Bottom Solder	Solder Resist	0.010mm	3.5
	Bottom Overlay			

PCB : TYPE 3

ASPECT-RATIO, AXE Z :
6 :1 à 8 :1
LEVEL "B"

MINIMUN PARAMETERS

DEFAULT
TRACKS : 0.150mm
GAPS : 0.150mm

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Hole Length
○	605	0.20mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	Via	-
□	2	1.00mm (39.37mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	-
	607 Total						

Project: STM32WL3 - QFN48 - 4-Layers - SMD

Layer: Drill Drawing

Variant: [No Variations]

Date: 12/02/2024

Gerber: .DRL

Ref: MB2218

Rev: A

The ST logo, consisting of a stylized 'S' and 'T' in a bold, italicized font, followed by a registered trademark symbol (®).