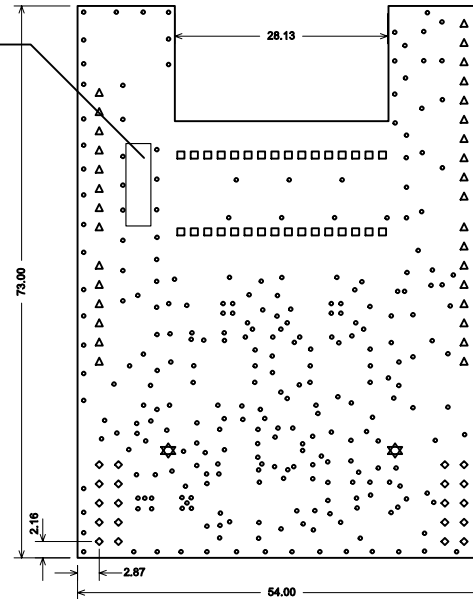


DATE CODE MARQUAGE FABRICANT
VERNIS EPARGNE FACE SOUDURE



DRILL TABLE

DRILLING THROUGH HOLES

SYMBOLE	DIAMETRE	NBR	PRECISION	METAL
•	0.3	295	VIA	YES
□	0.75	32	+/- 0.05	YES
◇	0.9	20	+/- 0.05	YES
△	1.1	32	+/- 0.05	YES
☆	2.0	2	+/- 0.05	NO

CROSS SECTION

1	Prepreg 2113	Cu 35µm
2	Core	Cu 17.5µm
3	Prepreg 2113	Cu 17.5µm
4		Cu 35µm

SIGNAL 50 OHMS :
LAYERS 1 & 4 -> TRACKS 150µm
PLANE REF
LAYER 1 -> LAYER 2
LAYER 3 -> LAYER 4

CARACTERISTIQUES DU CIRCUIT

CIRCUIT : 4 LAYERS
CLASS : 5
MATERIAL : FR4 TG150 VT47
SIZE CIRCUIT : 73mm X 54 mm
THICKNESS CIRCUIT : 1.6 mm
FINISH : NiAu Chimique
SOLDER RESIST : BLUE COLOR
☒ TOP ☒ BOTTOM
SILKSCREEN : WHITE COLOR
☒ TOP ☒ BOTTOM
DRILLING :
VIA PTH MINIMUM : 0.30mm

IE	DATE	MODIFICATION	ST MICROELECTRONICS	
			SIDE VIEW TOP ASSEMBLY	
			EWOK_VL53L3CX	
			DRILLING THROUGH HOLES	
C	13/11/2019	MODIFICATIONS SERIGRAPHIE	NUMBER: PCBDC181029	
B	10/01/2019	AJOUT DE U53, U54, U55	SCALE: 1:1	SHEET: 1/1
A	29/10/2018	CREATION	DESIGN DATE: 19/11/2019	DESIGNER:PTS

PCB-DESIGN
CONCEPT