

STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPower

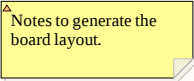
MB1848_LP

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- Sheet 1: Project overview (this page)
- Sheet 2: Reference_Design_STM32WL_4L_IPD_ST_V2_LP

Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.



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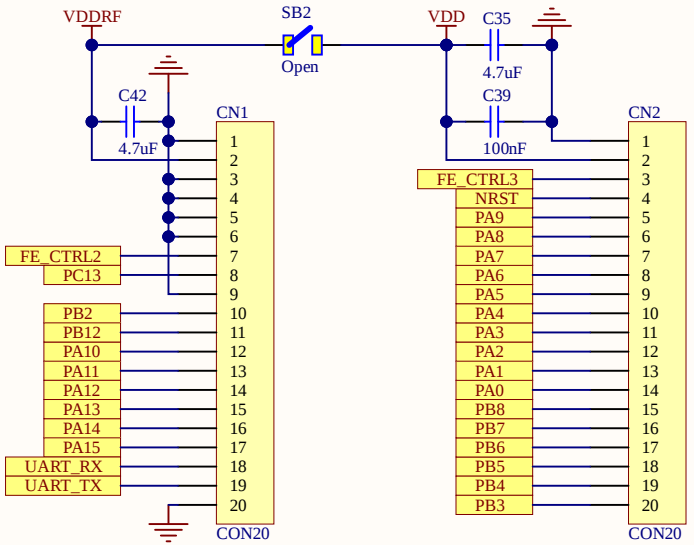
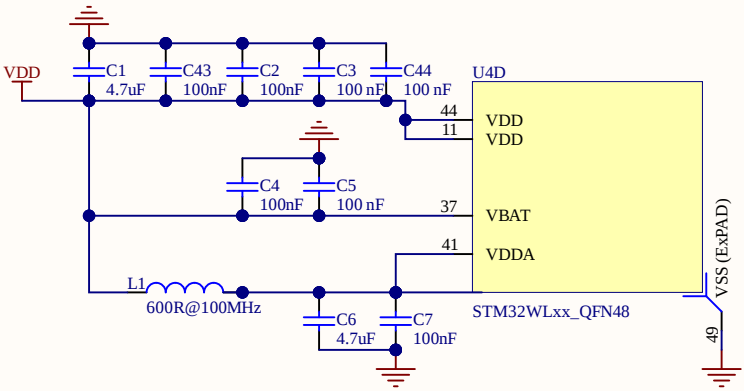
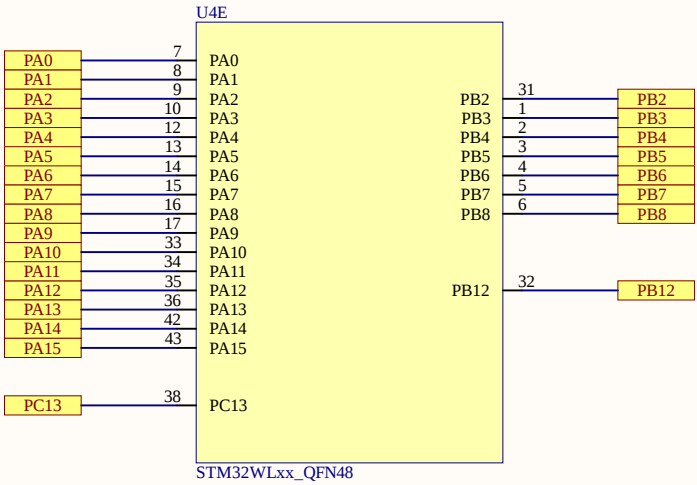
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Reference_Design_STM32WL_4L_IPD_ST_V2_LP.SchDoc



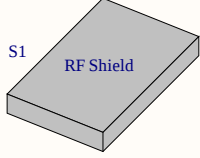
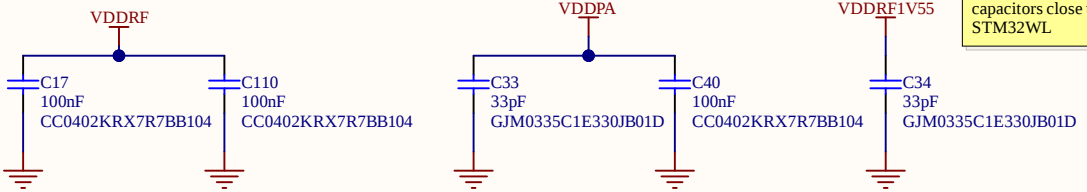
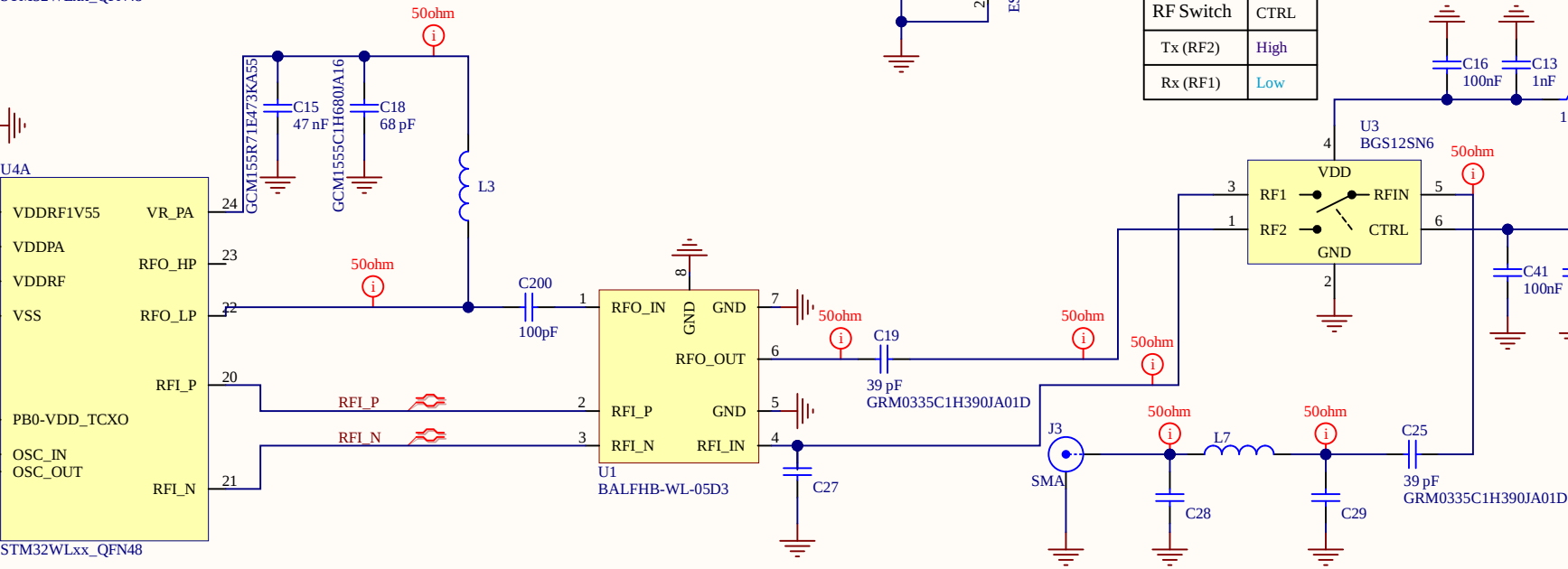
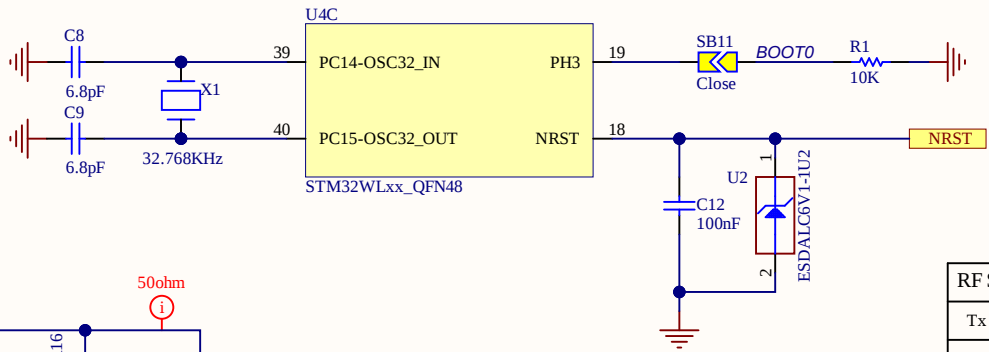
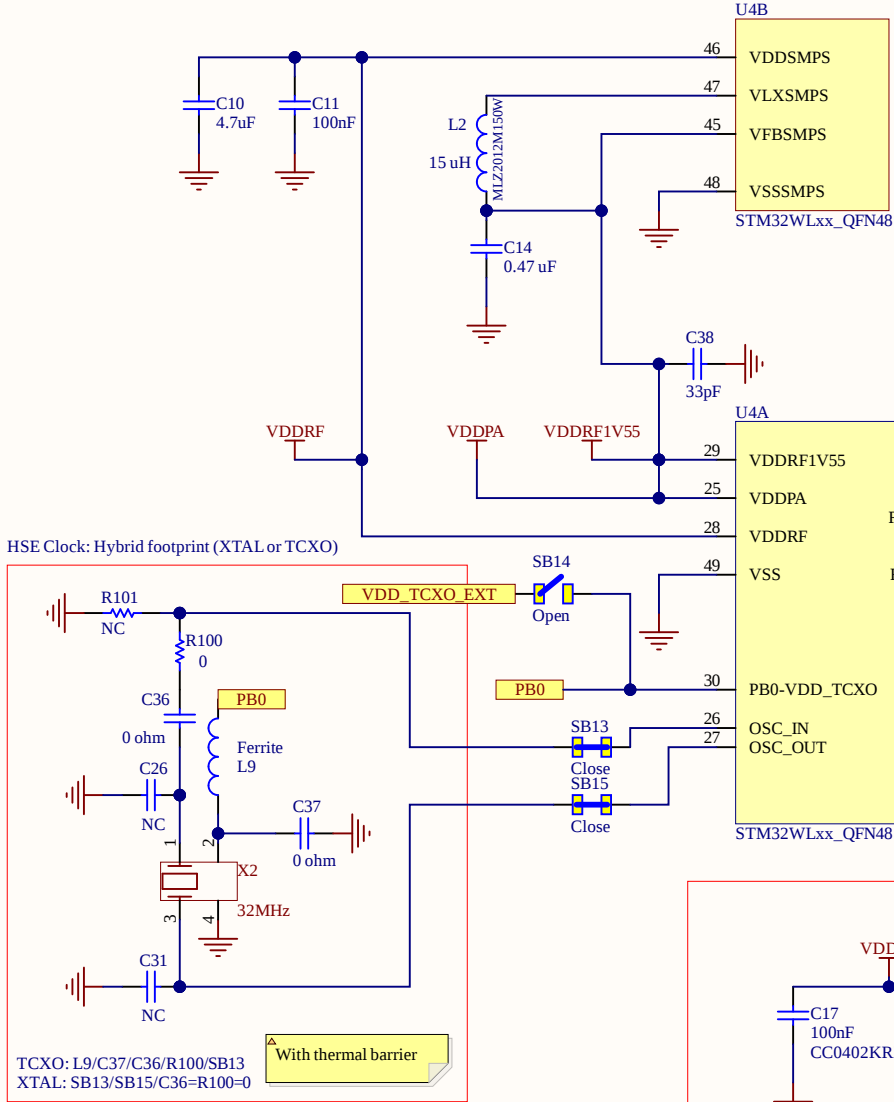
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Project: STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPow		
Variant: [No Variations]		
Revision: A-01		Reference: MB1848_LP
Size: A4	Date: June 2022	Sheet: 1 of 2



STM32WL QFN 4 Layers IPD - Low Power Mode - Reference Board



Connectors from daughter board (TOP LAYER)



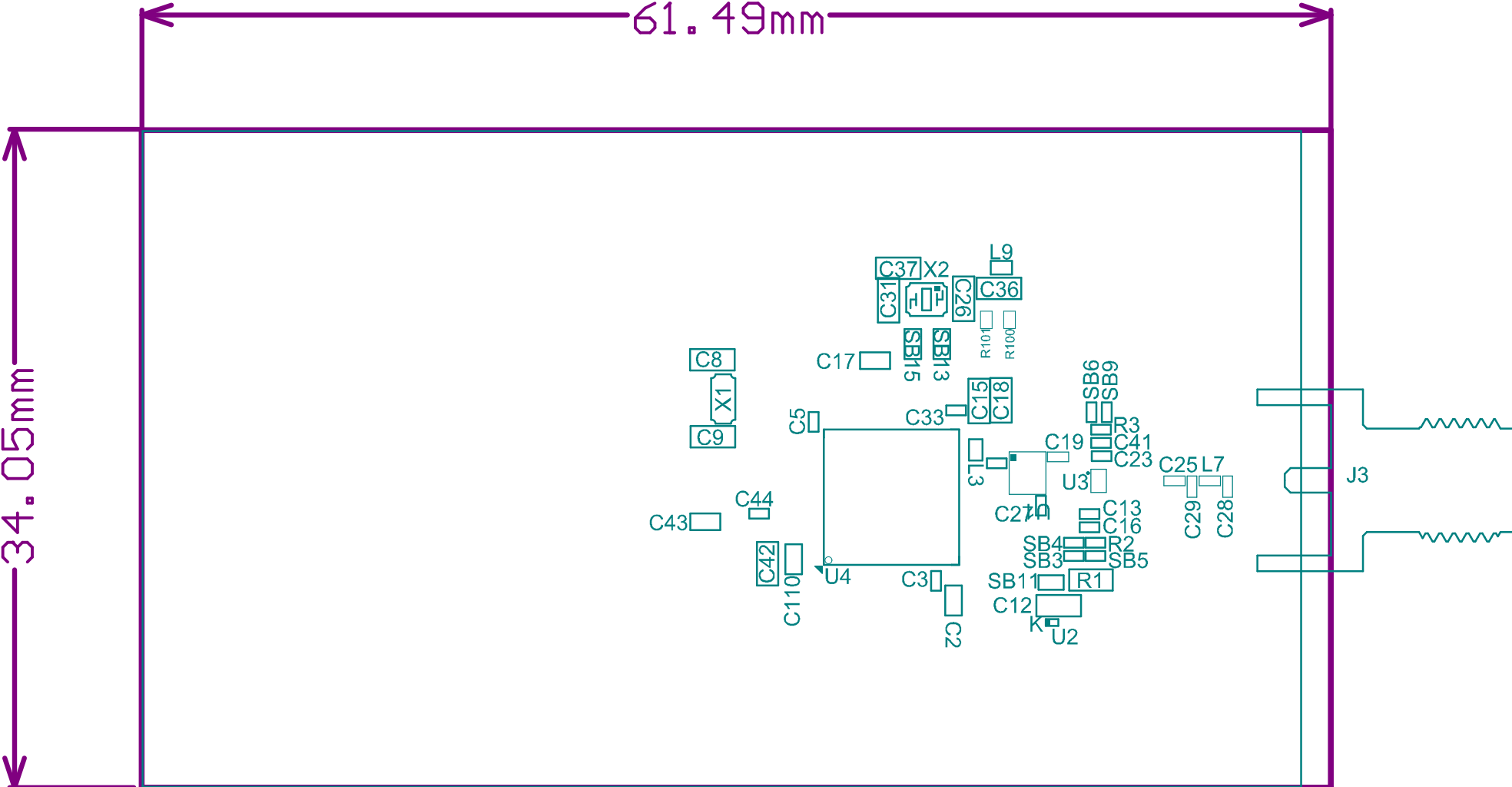
Notes:

RF BoM1[No Variations]

RF components:

- RF High-Q muRata GJM15 series for capacitors
- RF High-Q muRata LQW15AN series for inductors





Project: STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPower

Layer: **Top Assembly**

Gerber: **.GM14**

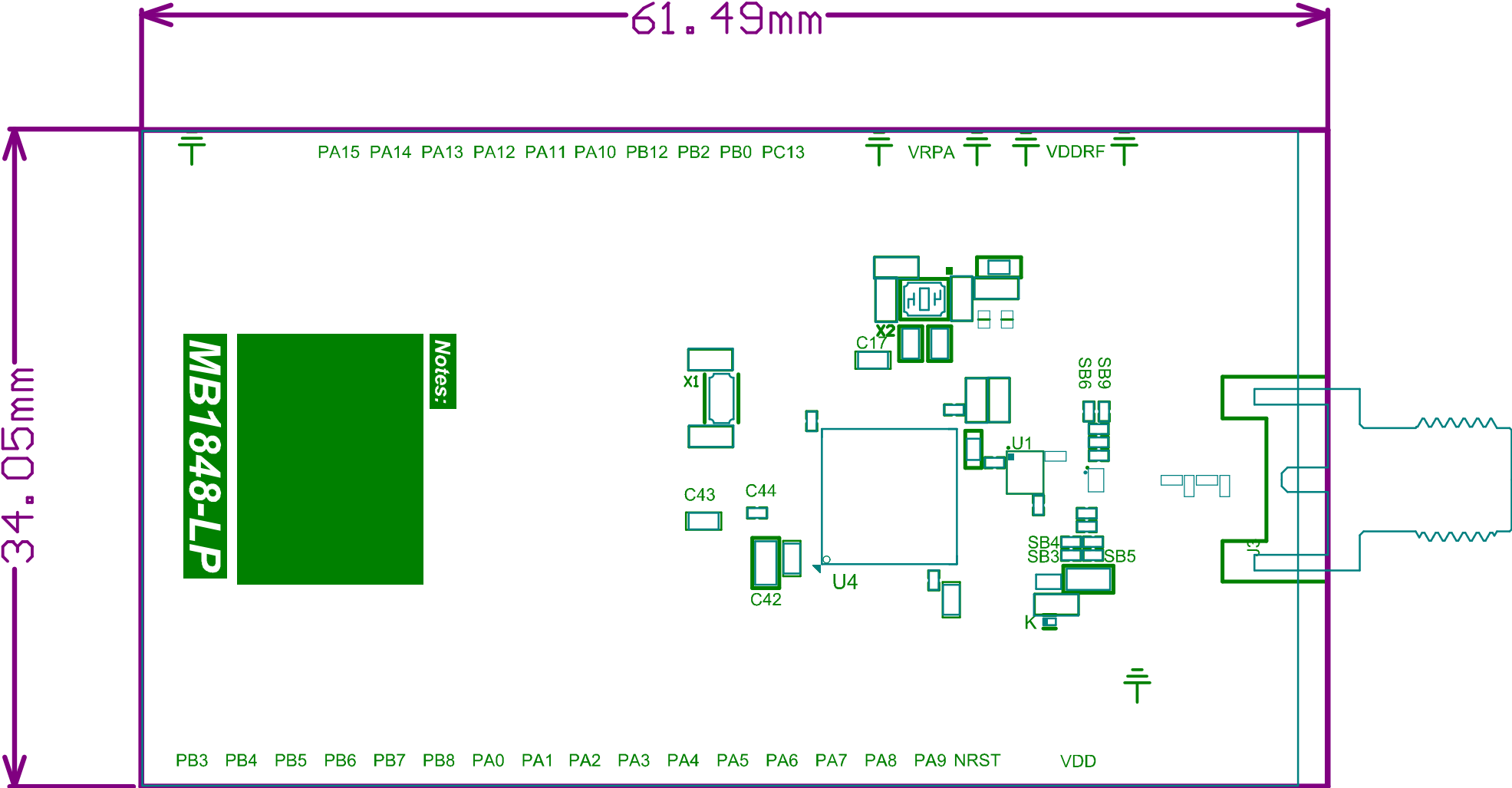
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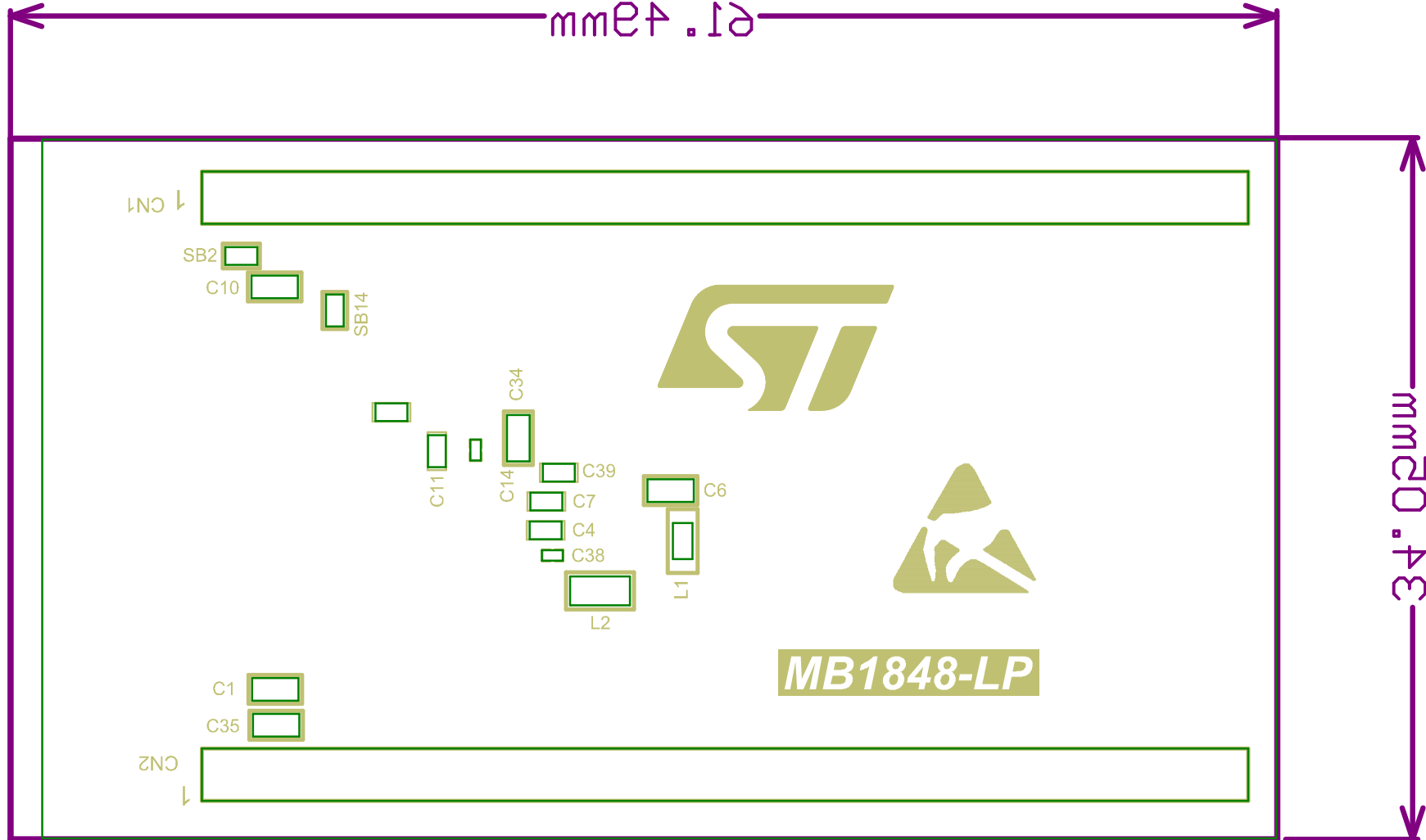
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Date: June 2022

Rev: A







Bottom Assembly

.GM15